

# **Curriculum Vitae of Matteo SONZA REORDA**

Matteo SONZA REORDA took his MS degree (Laurea) in Electronic Engineering from Politecnico di Torino (Torino, Italy) in 1986, and the PhD degree in Computer Engineering from the same Institution in 1990. Since 1990 he works with the Department of Control and Computer Engineering of Politecnico di Torino, where he is currently a Full Professor. In September 2021 he has been appointed as vice rector for Research of his university.

His main interests are in the test and reliable design of electronic circuits and systems, with special emphasis on functional test and safety, delay test, board test, reliability evaluation and software-based hardware fault tolerance. He published more than 400 papers on international journals and conference proceedings.

He received several awards for his research work, including the Best Paper award from the 9th IEEE International Conference on Tools with Artificial Intelligence (1997), the IEEE European Test Symposium (2005), the IEEE Conference on Design and Test in Europe (2005) and the IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems (2013 and 2021).

He has been the General and/or Program Chair of several international events, such as the IEEE On-Line Test Symposium (IOLTS), the IEEE European Test Symposium (ETS), the IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS), the IEEE Latin American Test Symposium (LATS), the IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC), the IEEE Symposium on Integrated Circuits and Systems Design (SBCCI). Since 2023 he is the chair of the Test track of the Design, Automation and Test in Europe Conference (DATE).

From 2010 to 2013 he has been the chair of the European branch of the IEEE Test Technology Technical Council (eTTTC). From 2016 to 2022 he has been the chair of the Steering Committee of the IEEE European Test Symposium. He is a member of the Steering Committees of the IEEE On-Line Test Symposium, of the IEEE European Test Symposium and of the IEEE Latin-American Test Symposium.

He was named a Fellow of the Institute of Electrical and Electronics Engineers (IEEE) in 2016 for his design of test algorithms for reliable circuits and systems.

He is also a member of the IFIP working group 10.5 (Design and Engineering of Electronic Systems) and a member of the Politecnico di Torino chapter of the IEEE Eta Kappa Nu (HKN) honor society.

Since May 2013 he is the co-director of the LIA LAFISI French-Italian research Laboratory on hardware-software Integrated Systems, funded by CNRS.

Since October 2018 he is a member of the Management Group of the “Programa Institucional de Internacionalização – Capes-PrInt” of the Pontificia Universidade Católica do Rio Grande do Sul in Porto Alegre (Brazil).

From November 2018 to October 2022 he was a member of the Advisory Board of the Graduate School “Intelligent Methods for Semiconductor Test and Reliability“ of the University of Stuttgart, in cooperation with Advantest.

Starting from April 2022, he is a member of the International Advisory Panel of DASHH “Data Science in Hamburg – Helmholtz Graduate School for the Structure of Matter”.

Matteo Sonza Reorda is the leader of the Electronic CAD research group within the Department of Control and Computer Engineering of Politecnico di Torino, which is currently composed of 9 faculty members and more than 15 PhD students and young researchers. In this role he has been / is the responsible for several research projects founded by public bodies

(such as the Italian Ministry for University, the Italian Space Agency, the European Commission) and private industries (such as STMicroelectronics, Intel, NVIDIA, NEC USA, Magneti Marelli, Pirelli, Larimart, SPEA).

Further information on Matteo SONZA REORDA's curriculum can be found on his web site ([www.cad.polito.it/~sonza](http://www.cad.polito.it/~sonza)).